By

	Application No.	Applicant(s)
Notice of Allowability	10/791,350	PATEL, ANAND RATIBHAI
	Examiner	Art Unit
	Raymond Phan	2111
The MAILING DATE of this communication app All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85 NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT R of the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED in or other appropriate commeting the commeting in the commeting in the commeting is the commeting in the	n this application. If not included unication will be mailed in due course. THIS
1. This communication is responsive to the amendment filed	on September 13, 2007.	
2. \boxtimes The allowed claim(s) is/are $\underline{1-2,4-5,7-10,12-15,17-20}$ and	<u>22</u> .	
 3. Acknowledgment is made of a claim for foreign priority u a) All b) Some* c) None of the: 1. Certified copies of the priority documents have 2. Certified copies of the priority documents have 3. Copies of the certified copies of the priority do 	e been received. e been received in Application	on No
International Bureau (PCT Rule 17.2(a)).		a in the national stage approached from the
* Certified copies not received:		
Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONN THIS THREE-MONTH PERIOD IS NOT EXTENDABLE. 4. A SUBSTITUTE OATH OR DECLARATION must be subm	MENT of this application.	
INFORMAL PATENT APPLICATION (PTO-152) which giv	es reason(s) why the oath o	r declaration is deficient.
5. CORRECTED DRAWINGS (as "replacement sheets") mu		
(a) including changes required by the Notice of Draftsper	•	w (PTO-948) attached
1) hereto or 2) to Paper No./Mail Date		all the Office action of
(b) including changes required by the attached Examiner Paper No./Mail Date	•	
Identifying indicia such as the application number (see 37 CFR 1 each sheet. Replacement sheet(s) should be labeled as such in		
6. DEPOSIT OF and/or INFORMATION about the deposit attached Examiner's comment regarding REQUIREMENT		
,	·	
Attachment(s)		
1. Notice of References Cited (PTO-892)		formal Patent Application
2. Notice of Draftperson's Patent Drawing Review (PTO-948)		ummary (PTO-413), /Mail Date <u>09242007</u> .
Information Disclosure Statements (PTO/SB/08), Paper No./Mail Date		Amendment/Comment
4. Examiner's Comment Regarding Requirement for Deposit	8. 🛭 Examiner's	Statement of Reasons for Allowance
of Biological Material	9.	Raymoff
		Raymond Phan Patent Examiner Tech Center 2100

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Allowable Subject Matter

1. Claims 1-5, 7-10, 12-15, 17-20, 22 are allowable over the prior art of records.

2. The following is an Examiner's statement of reasons for the indication of allowable subject matter: Claims 1, 8, 14, 18 are allowable over the prior art of record because the prior arts, cited in its entirety or to combine any of the prior arts, do not teach a processor to receive an interrupt signal intended for a system processor from the expansion device on the expansion bus in a command path and to prevent the interrupt signal from reaching the system processor in the command path.

The remaining claims, not specifically mentioned, are allowed for the same rationale as set for their dependent claims.

- 3. Any comments considered necessary by applicant must be submitted no later than the payment of the Issue Fee and, to avoid processing delays, should preferable **accompany** the Issue Fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."
- 4. Any inquiry concerning this communication or earlier communications from the examiner should be directed to examiner Raymond Phan, whose telephone number is (571) 272-3630. The examiner can normally be reached on Monday-Friday from 7:30AM-4:00 PM. The Group Fax No. (571) 273-8300.

Communications via Internet e-mail regarding this application, other than those under 35 U.S.C. 132 or which otherwise require a signature, may be used by the applicant and should be addressed to [raymond.phan@uspto.gov].

All Internet e-mail communications will be made of record in the application file. PTO employees do not engage in Internet communications where there exists a possibility that sensitive information could be identified or exchanged unless the record includes a properly signed express waiver of the confidentiality requirements of 35 U.S.C. 122. This is more clearly set forth in the Interim Internet Usage Policy published in the Official Gazette of the Patent and Trademark on February 25, 1997 at 1195 OG 89.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see hop://pair-direct.uspto.gov. Should

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you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Any inquiry of a general nature or relating to the status of this application should be directed to the TC2100 central telephone number is (571) 272-2100.

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Examiner's Amendement

2. An Examiner's Amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 C.F.R. 3 1.312. To ensure consideration of such an amendment, it **MUST** be submitted no later than the payment of the Issue Fee.

Authorization for this Examiner's Amendment was given in a telephone interview with Ms. Julie Reed (Reg. #35,349) on September 24, 2007.

- 3. The application has been amended as follows:
 - 1. A network bridge device, comprising:
 - a first port to allow the <u>bridge</u> device to communicate with other devices on an expansion bus;
 - a second port to allow the <u>bridge</u> device to communicate with devices on a system bus;
 - a memory to store data; and
 - a processor to:

receive a set of data from an expansion device on the expansion bus in a data path;

receive an interrupt signal intended for a system processor from the expansion device on the expansion bus in a command path and to prevent the interrupt signal from reaching the system processor in the command path;

transmit the set of data into a transaction queue in a data path to the system processor;

generate an indicator of completion to the system processor to indicate the completion of the data transmission to the system processor; and

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insert the indicator into the transaction queue in the data path to a system memory associated with the system processor after the set of data; generate a read request to the expansion device.

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- 2. The network <u>bridge</u> device of claim 1, the <u>network bridge</u> device further comprising a peripheral component interconnect bridge.
- 4. The <u>network bridge</u> device of claim 1, wherein the second port allows the <u>bridge</u> device to communicate with devices on an expansion bus.
- 5. The network bridge device of claim 1, the processor to generate an indicator of completion further comprising the processor to generate a transaction addressed to a predetermined area of a system memory.
- 7. The <u>network bridge</u> device of claim 61, the processor to insert the indicator further comprising the processor to transmit data from the read request to a predetermined address in a system memory.
- 14. A network bridge device, comprising:

a means for allowing the <u>bridge</u> device to communicate with other devices on a first bus, the first bus being an expansion bus;

a means for allowing the <u>bridge</u> device to communicate with devices on a second bus the second bus being a system bus;

a means for storing data; and

a means for:

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receiving a set of data from an expansion device on the expansion bus in a data path;

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intercepting an interrupt signal intended for a system processor from the expansion device on the expansion bus in a command path and preventing the interrupt signal from progressing to the system processor; inserting the set of data into a transaction queue in data path to the system processor;

generating an indicator of completion to the system processor to indicate the completion of the data transmission to the system processor; and inserting the indicator into the transaction queue in the data path to a system memory associated with the system processor after the set of data; generating a read request to the expansion device.

- 15. The <u>network bridge</u> device of claim [1] <u>14</u>, the <u>network bridge</u> device further comprising a peripheral component interconnect bridge.
- 17. The network bridge device of claim [1] 14, the means for allowing the bridge device to communicate with device on a second bus further comprising a means to allow the bridge device to communicate on a second expansion bus.
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